

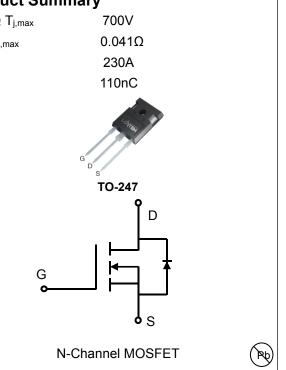
Lonten N-channel 650V, 78A, 0.041Ω LonFET[™] Power MOSFET

Description **Product Summary** LonFET[™] Power MOSFET is fabricated using V_{DS} @ T_{j,max} 700V advanced super junction technology. The resulting 0.041Ω R_{DS(on),max} 230A device has extremely low on resistance, making it IDM especially suitable for applications which require 110nC Q_{g,typ} superior power density and outstanding efficiency. **Features** Ultra low R_{DS(on)} ٠ Ultra low gate charge (typ. $Q_g = 110nC$) TO-247 ٠ 100% UIS tested ٠

RoHS compliant ٠

Applications

- Power faction correction (PFC). ٠
- Switched mode power supplies (SMPS). ٠
- Uninterruptible power supply (UPS). ٠



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	650	V
Continuous drain current ($T_c = 25^{\circ}C$)	ID	78	А
(T _c = 100°C)		46	А
Pulsed drain current ¹⁾	I _{DM}	230	А
Gate-Source voltage	V _{GSS}	±30	V
Avalanche energy, single pulse 2)	E _{AS}	2350	mJ
Power Dissipation TO-247 ($T_c = 25^{\circ}C$)	5	500	W
- Derate above 25°C	PD	4.0	W/°C
Operating and Storage Temperature Range	Tj, Tstg	-55 to +150	°C
Continuous diode forward current	Is	78	А
Diode pulse current	I _{S,pulse}	230	А

Thermal Characteristics TO-247

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	0.25	°C/W
Thermal Resistance, Junction-to-Ambient	R _{0JA}	62	°C/W
Soldering temperature, wavesoldering only allowed	T _{sold}	260	°C
at leads. (1.6mm from case for 10s)	. 5014		



Package Marking and Ordering Information

Device	Device Package	Marking	Units/Tube	Units/Reel
LSB65R041GF	TO-247	LSB65R041GF	30	

Electrical Characteristics T_c = 25°C unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Static characteristics						
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0 V, I _D =0.25 mA	650	-	-	V
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =0.25 mA	2.5	3.5	5.0	V
Drain cut-off current	I _{DSS}	V _{DS} =650 V, V _{GS} =0 V,				μA
		T _j = 25°C	-	-	5	
		T _j = 125°C	-	10	-	
Gate leakage current, Forward	IGSSF	V _{GS} =30 V, V _{DS} =0 V	-	-	100	nA
Gate leakage current, Reverse	I _{GSSR}	V _{GS} =-30 V, V _{DS} =0 V	-	-	-100	nA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =10 V, I _D =39 A	-			
		T _j = 25°C	-	0.036	0.041	Ω
		T _j = 150°C	-	0.094	-	
Dynamic characteristics			·			
Input capacitance	Ciss	V _{DS} = 100 V, V _{GS} = 0 V,	-	7710	-	
Output capacitance	Coss	f = 250 kHz	-	252	-	pF
Reverse transfer capacitance	C _{rss}		-	6.66	-	
Turn-on delay time	t _{d(on)}	V _{DD} = 400 V, I _D = 39 A	-	46	-	
Rise time	tr	R _G = 10 Ω, V _{GS} =10 V	-	52	-	ns
Turn-off delay time	t _{d(off)}		-	342	-	
Fall time	t _f	-	-	8.6	-	
Gate charge characteristics	L	1	I		1	1
Gate to source charge	Q _{gs}	V _{DD} =400 V, I _D =39 A,	-	25.7	-	
Gate to drain charge	Q _{gd}	V _{GS} =0 to 10 V	-	42.2	-	nC
Gate charge total	Qg		-	110	-	
Gate plateau voltage	V _{plateau}	1	-	6.0	-	V
Reverse diode characteristics		•				
Diode forward voltage	V _{SD}	V _{GS} =0 V, I _F =39 A	-	-	1.2	V
Reverse recovery time	trr	V _R =50 V, I _F =39 A,	-	200	-	ns
Reverse recovery charge	Qrr	dl _F /dt=100 A/µs	-	1.9	-	μC
Peak reverse recovery current	Irrm		-	18.3	-	Α

Notes:

1. Limited by maximum junction temperature, maximum duty cycle is 0.75.

2. I_{AS} = 10A, V_{DD} =60V, Starting T_j= 25°C.



Electrical Characteristics Diagrams

Figure 1. On-Region Characteristics

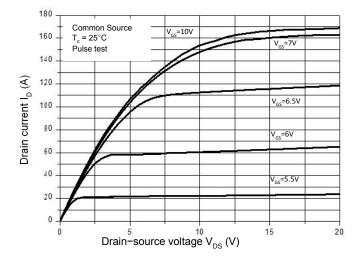


Figure 3. On-Resistance Variation vs. Drain Current

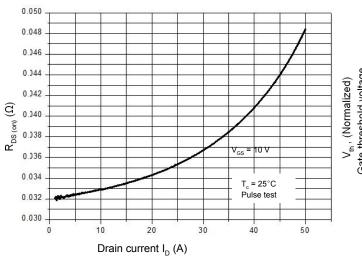


Figure 5. Breakdown Voltage vs. Temperature

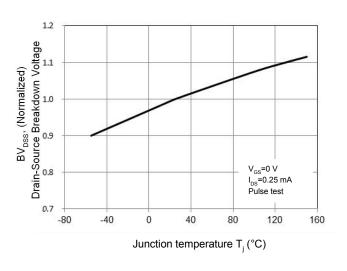


Figure 2. Transfer Characteristics

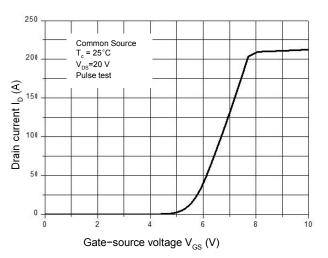


Figure 4. Threshold Voltage vs. Temperature

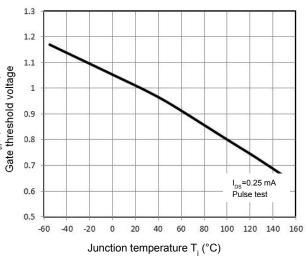


Figure 6. On-Resistance vs. Temperature

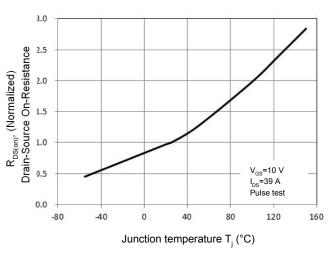
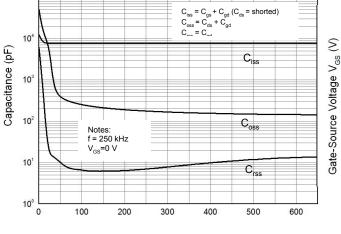




Figure 7. Capacitance Characteristics



Drain-Source Voltage V_{DS} (V)



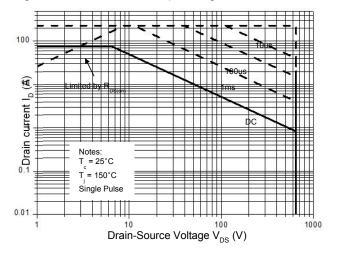
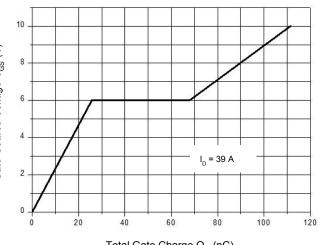
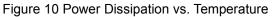
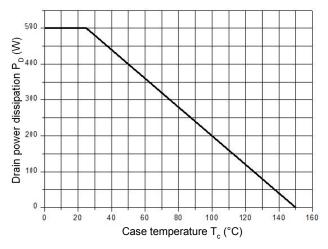


Figure 8. Gate Charge Characterist



Total Gate Charge Q_G (nC)

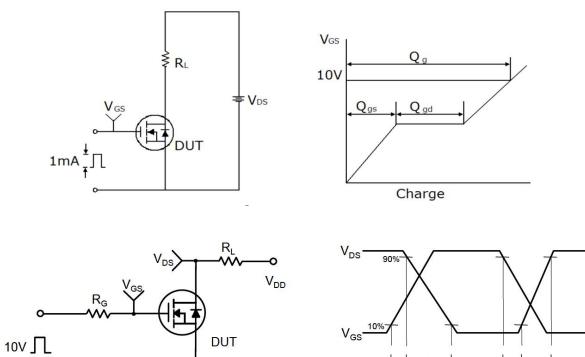






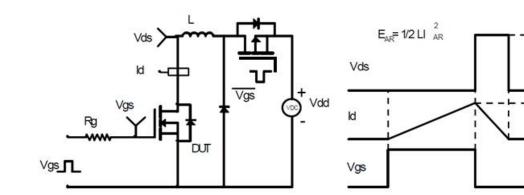
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Gate Charge Test Circuit & Waveform



Unclamped Inductive Switching Test Circuit & Waveforms

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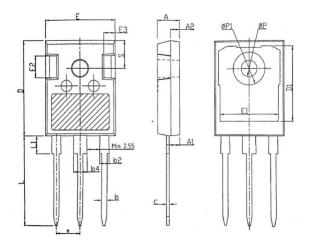


BV_{DSS}

LAR

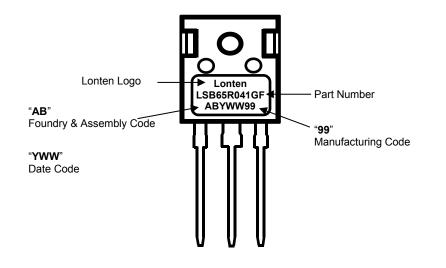


Mechanical Dimensions for TO-247



SYMBOL		mm	
STIVIBOL	MIN	NOM	МАХ
А	4.80	5.00	5.20
A1	2.21	2.41	2.59
A2	1.85	2.00	2.15
b	1.11	1.21	1.36
b2	1.91	2.01	2.21
b4	2.91	3.01	3.21
с	0.51	0.61	0.75
D	20.80	21.00	21.30
D1	16.25	16.55	16.85
E	15.50	15.80	16.10
E1	13.00	13.30	13.60
E2	4.80	5.00	5.20
E3	2.30 2.50		2.70
е	5.44BSC		
L	19.82	19.92	20.22
L1	_	_	4.30
ØP	3.40	3.60	3.80
ØP1	_	_	7.30
S		6.15BSC	

TO-247 Part Marking Information





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